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Inventor(s)

Title

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## UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

: 35824/DBP/A400

: Ichirou Kawamura

: IMAGE DISTRIBUTING AND PROCESSING APPARATUS

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09/391272

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ADDRESS TO: Assistant Commissioner for Patents

Box Patent Application

Washington, D.C. 20231

Date: September 7, 1999

1. X **FEE TRANSMITTAL FORM** (*Submit an original, and a duplicate for fee processing*).2. **IF A CONTINUING APPLICATION**       This application is a        of patent application No. .       This application claims priority pursuant to 35 U.S.C. §119(e) and 37 CFR §1.78(a)(4), to provisional Application No. .3. **APPLICATION COMPRISED OF****Specification**12 Specification, claims and Abstract (total pages)**Drawings**5 Sheets of drawing(s) (FIGS. 1 to 7)**Declaration and Power of Attorney**X Newly executed       No executed declaration       Copy from a prior application (37 CFR 1.63(d))(for continuation and divisional)4.        **Microfiche Computer Program** (*Appendix*)5.        **Nucleotide and/or Amino Acid Sequence Submission** (*if applicable, all necessary*)       Computer Readable Copy       Paper Copy (identical to computer copy)       Statement verifying identity of above copies6. **ALSO ENCLOSED ARE**       Preliminary Amendment       A Petition for Extension of Time for the parent application and the required fee are enclosed as separate papers       Small Entity Statement(s)       Statement filed in parent application, status still proper and desired       Copy of Statement filed in provisional application, status still proper and desired

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**UTILITY PATENT APPLICATION TRANSMITTAL**  
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Docket No.: 35824/DBP/A400

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- ☒ An Assignment of the invention with the Recordation Cover Sheet and the recordation fee are enclosed as separate papers
- ☐ This application is owned by pursuant to an Assignment recorded at Reel , Frame
- ☐ Information Disclosure Statement (IDS)/PTO-1449
- ☐ Copies of IDS Citations
- ☒ Certified copy of Priority Document(s) (*if foreign priority is claimed*)
- ☐ English Translation Document (*if applicable*)
- ☒ Return Receipt Postcard (MPEP 503) (should be specifically itemized).
- ☐ Other

**7. CORRESPONDENCE ADDRESS**

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Respectfully submitted,

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By

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626/795-9900

DBP/sfc

IMAGE DISTRIBUTING AND PROCESSING APPARATUS

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to an image distributing and processing apparatus in which images are processed by different image processors in accordance with parameters which are used to process images on a surface of a semiconductor integrated device, such as a semiconductor wafer, a photo-mask, or a liquid crystal display, etc.

In general, the brightness of the surface of the semiconductor integrated device, a pattern density, or the material are different depending on the surface portions of the same semiconductor integrated device. To process images at different surface portions, it is advisable to set different parameters depending on the state of the surface portions to be processed.

2. Description of the Related Art

In a known image processing apparatus, once an image of one semiconductor integrated device is input into an image processor, the image is processed using a parameter inherent to each characteristic, whatever the characteristic, such as the surface contrast, pattern density or the material, is. Therefore, for example, if the image is to be corrected in the brightness thereof, since the parameter inherent to the brightness is constant over the entire surface of the image, it is impossible to correct the image in accordance with the brightness of each surface portion. The same is true for the correction of the image in accordance with the pattern density or material, etc.

Therefore, if a surface portion of the semiconductor integrated device requires an image processed using a parameter and another surface portion requires an image processed using a different parameter,

an image of the surface of the semiconductor integrated device is picked up and is processed using a parameter and thereafter, an image of the surface of the semiconductor integrated device is picked up and processed using another parameter, in the prior art.

Therefore, in the prior art, in order to process images of different surface portions of a semiconductor integrated device, using different parameters, it is necessary to obtain images the number of which is identical to the number of the parameters and to apply different parameters to the respective images to thereby combine the images corresponding to the parameters, whereby a corrected image can be obtained. Accordingly, the image processing operation is extremely troublesome and requires long time.

#### SUMMARY OF THE INVENTION

The primary object of the present invention is to provide an image distributing and processing apparatus in which the efficiency of the image processing operation using different parameters corresponding to the surface characteristics of a semiconductor integrated device can be enhanced.

Another object of the present invention is to provide an image distributing and processing apparatus in which only a desired image can be selected, based on the outcome of the image processing operation.

To achieve the object mentioned above, according to the present invention, there is provided an image distributing and processing apparatus comprising an input data storing device in which input image data is stored in a predetermined order; a plurality of image processing/storing devices; a distribution table in which a relationship between addresses of the input data storing device and addresses of output data outputted from the plural image processing/storing devices is stored in advance in accordance with parameters to be used for the image processing operation; a distributor

which distributes the image data outputted from the input data storing device into the plural image processing/storing devices in accordance with the content of the distribution table; a reconfiguration device which reconfigures the output image data processed and stored by the plural image processing/storing devices in the above mentioned predetermined order, with reference to the distribution table; and an output data storing device which stores therein output image data outputted from the reconfiguration device.

With the preparation of the distribution table, the input data can be distributed to different image processing/storing devices in accordance with different parameters and, hence an operation to obtain and process an image every time the parameter is modified is not needed. Consequently, not only can the image be processed within a short space of time but also the efficiency of the image processing operation can be enhanced.

It is possible to use a reconfiguration table in which a relationship between addresses of a part of output data areas outputted from the plural image processing/storing devices in accordance with each parameter and the addresses of the output data storing device is stored in advance, in place of the distribution table. The image data can be reconfigured referring to the reconfiguration table and can be stored in the output data storing device.

Since only a part of the output data area outputted from the image processing/storing devices is stored in the output data storing device, only the effective data must be stored in the output data storing device and thus the evaluation of the output image can be simplified.

#### BRIEF EXPLANATION OF THE DRAWINGS

The invention will be discussed below in detail with reference to the accompanying drawings, in which;

Fig. 1 is a block diagram of an internal structure

of an image distributing and processing apparatus according to a first embodiment of the present invention;

Fig. 2 is a diagram showing the contents of a distribution table shown in Fig. 1, by way of example;

Fig. 3 is a schematic view showing a relationship between addresses of an input data storage device 2 and addresses of image processing/storing devices 8-1 to 8-n, shown in Fig. 1, by way of example;

Fig. 4 is a block diagram of an internal structure of an image distributing and processing apparatus according to a second embodiment of the present invention;

Fig. 5 is a schematic view showing relationships between memory areas of an input data storage device 2, the memory areas of image processing/storing devices 8-1 to 8-n and memory areas of an output data storage device 12 shown in Fig. 4, by way of example;

Fig. 6 is a diagram showing a part of the contents of a distribution table shown in Fig. 4; and

Fig. 7 is a diagram showing a part of the contents of a reconfiguration table shown in Fig. 4.

#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

##### <Embodiment 1>

Fig. 1 shows a block diagram of a first embodiment of an image distributing and processing apparatus according to the present invention. In Fig. 1, the image distributing and processing apparatus is comprised of an input data storage device 2 which stores therein input image data in a predetermined sequence, a plurality of image processing/storing devices 8-1, 8-2, ... 8-n, a distribution table in which a correspondence between the addresses of the input data storing device and the addressed of the output data from the plural image processing/storing devices 8-1 to 8-n is stored in advance, corresponding to parameters to be used in the image processing operation, a distributor 6 which distributes the image data outputted from the input data

storing device into the plural image processing/storing devices in accordance with the content of the distribution table, a reconfiguration device 10 which reconfigures the output image data processed and stored in the plural image processing/storing devices 8-1 to 8-n in a predetermined sequence, referring to the distribution table, and an output data storing device 12 which stores therein the output image data outputted from the reconfiguration device 10.

In operation, the input image data which is obtained by picking up the surface of an object to be examined, using an image pickup device (not shown) which is comprised, in combination, of an optical microscope and an image pickup element such as TDI (Time Delay Integration) is stored serially or in parallel in the input data storing device 2. The distributor 6 distributes the image data outputted from the input data storing device 2 into the image processing/storing devices 8-1 to 8-n corresponding to the addresses of the input data storing device 2, with reference to the distribution table 4. Each image processing/storing device processes the input image data and stores the outcome in the memory areas designated by the distribution table 4. The reconfiguration device 10 reconfigures the output image data supplied from the image processing/storing devices 8-1 to 8-n in the same order as those stored in the input data storing device 2, with reference to the distribution table 4 and stores the same in the output data storing device 12.

Fig. 2 shows an example of the content of the distribution table shown in Fig. 1. In Fig. 2, X0 to X11 represent addresses of the input data storing device 2; a0 to a5 represent the addresses of the image processing/storing device 8-1; b0 to b3 represent the addresses of the image processing/storing device 8-2; c0 to c2 represent the addresses of the image processing/storing device 8-n, respectively. In this

distribution table 4, the addresses (X0, X1) of the input data storing device correspond to the addresses (a0, a1) of the image processing/storing device 8-1 and the addresses (X2, X3) of the input data storing device correspond to the addresses (b0, b1) of the image processing/storing device 8-2, respectively. The same is true for the remaining addresses of the input data storing devices and image processing/storing devices.

The correspondence is predetermined based on the images at each object image pickup stage to show parameters to be used for the corresponding image portions.

Fig. 3 shows an example of a relationship between the addresses of the input data storing device 2 and those of the image processing/storing devices 8-1 to 8-n, shown in Fig. 1. In the illustrated embodiment, the data within the input data storing device 2 includes data 20 of the addresses X0 to X1, data 22 of the addresses X2 to X3, data 24 of the addresses X4 to X5, data 26 of the addresses X6 to X7, data 28 of the X8 to X9 and data 30 of the addresses X10 to X11.

The data 20, 22, 24, 26, 28 and 30 are data to be image-processed using different parameters such as different contrasts. Each address represents that of an end portion of each data area in the illustrated embodiment. Therefore, the address next to the last address X1 within the data area 20 is the first address X2 in the data area 22. The same is true for the remaining addresses. Although the data areas are continuous in the drawing, it is possible to provide a gap between the data areas.

As can be seen from the drawings, in accordance with the content of the distribution table shown in Fig. 2, the addresses (X0, X1) of the input data storing device 2 correspond to the addresses (a0, a1) of the image processing/storing device 8-1; the addresses (X2, X3) of the input data storing device 2 correspond to the



addresses (b0, b1) of the image processing/storing device 8-2; the addresses (X4, X5) of the input data storing device 2 correspond to the addresses (a2, a3) of the image processing/storing device 8-1; the addresses (X6, X7) of the input data storing device 2 correspond to the addresses (c0, c1) of the image processing/storing device 8-n; the addresses (X8, X9) of the input data storing device 2 correspond to the addresses (b2, b3) of the image processing/storing device 8-2; the addresses (X10, X11) of the input data storing device 2 correspond to the addresses (a4, a5) of the image processing/storing device 8-1, respectively.

According to the relationship mentioned as above, for example, the input data 20 is distributed to the corresponding image processing/storing device 8-1 and is processed using a parameter inherent to the device. The process result is stored at the addresses a0 to a1 of the device.

The reconfiguration device 10 rearranges the processed image data stored in the image processing/storing devices 8-1 to 8-n in the original order, with reference to the distribution table 4 and stores the same in the output data storing device 12.

Thus, the images can be processed in parallel using different parameters for the respective data areas of the input image, using the input image data which has been obtained by a single pickup operation and, hence the image processing operation efficiency can be enhanced.

<Embodiment 2>

Fig. 4 shows a block diagram of a second embodiment of an image distributing and processing apparatus according to the present invention. In Fig. 4, the elements corresponding to those in Fig. 1 are designated by like numerals and duplicate explanations will not be given below. A difference, compared to Fig. 1, resides in the point that the reconfiguration device 10 refers to a reconfiguration table in place of reference to the

distribution table.

In general, all the image data obtained as a result of the image processing operation are not always used as output data. For instance, in case that the mean value of the gray level of  $3 \times 3$  pixels with respect to a central image pixel or the range value which represents a difference between the maximum value and the minimum value of the gray level is determined to be a gray level of the central pixel, since no mean value or range value of the pixels at the end portions the image can be obtained, the pixels at the end portions of the image must be excluded from the output data. Alternatively, there is a case that only necessary data is to be outputted from the processed image data.

To this end, in the second embodiment, the reconfiguration table 14 stores in advance a relationship between the areas corresponding to the output data areas outputted from the plural image processing/storing devices 8-1 to 8-n corresponding to each parameter from which the output data areas the opposed edge portions have been removed and the addresses of the memory areas of the output data storing device 12.

Fig. 5 shows a relationship between the memory area of the input data storing device 2, the memory area of the image processing/storing device 8-1 and the memory area of the output data storing device 12, in the second embodiment. Fig. 6 shows a part of the contents of the distribution table 4 in the second embodiment. Fig. 7 shows a part of the contents of the reconfiguration table 14 in the second embodiment. In an example shown in Fig. 5, the data between the address from X0 to X3 of the input data storing device 2 includes, at its opposed ends, edge portions 51 and 52. The addresses of the edge portion 51 are assigned X0 to X1. The addresses of the edge portion 52 are assigned X2 to X3. As may be understood from Figs. 5 to 7, the data at the addresses X0 to X3 of the input data memory device 2 is processed

by the image processing/storing device 8-1 in the same way as that in the first embodiment, and is stored at the addresses a0 to a3 in accordance with the distribution table 4. Among the stored image data, the addresses of the edge portion 51a are a0 to a1 and those of the edge portion 52a are a2 to a3. The reconfiguration device 10 stores only the data at the addresses a1 to a2, within the image processing/storing device 8-1, in the output data storing device 12 in accordance with the content of the reconfiguration table 14 without transferring the data of the edge portions 51a and 52a to the output data storing device 12.

The remaining image processing/storing devices are not shown in Fig. 5, but the reconfiguration table specifies the relationship between the addresses of the image processing/storing devices and the addresses of the output data storing device 12. Consequently, the data from the respective image processing/storing devices are stored in a predetermined order into the output data storing device 12 in the same way as shown in Fig. 5. Thus, only the necessary data is stored in the output data storing device 12.

As can be understood from the above discussion, according to the present invention, since the images can be processed in parallel in accordance with different parameters for each data area of the input image, using input image data which has been obtained by a single image pickup operation, the efficiency of the image processing operation can be increased. Moreover, processed data from which unwanted data portions have been removed can be obtained.

What is Claimed is:

1. An image distributing and processing apparatus comprising:

an input data storing device in which  
5 input image data is stored in a predetermined order;  
a plurality of image processing/storing  
devices;  
a distribution table in which a  
relationship between addresses of the input data storing  
10 device and addresses of output data outputted from the  
plural image processing/storing devices is stored in  
advance in accordance with parameters to be used for the  
image processing operation;

a distributor which distributes the image  
15 data outputted from the input data storing device into  
the plural image processing/storing devices in accordance  
with the content of the distribution table;

a reconfiguration device which  
reconfigures the output image data processed and stored  
20 by the plural image processing/storing devices in the  
above mentioned predetermined order, with reference to  
the distribution table; and

an output data storing device which stores  
therein output image data outputted from the  
25 reconfiguration device.

2. An image distributing and processing apparatus comprising:

an input data storing device in which  
input image data is stored in a predetermined order;  
30 a plurality of image processing/storing  
devices;

a distribution table in which a  
relationship between addresses of the input data storing  
device and addresses of output data outputted from the  
35 plural image processing/storing devices is stored in  
advance in accordance with parameters to be used for the  
image processing operation;

a distributor which distributes the image data outputted from the input data storing device into the plural image processing/storing devices in accordance with the content of the distribution table;

5 a reconfiguration device which reconfigures the image data processed by the plural image processing/storing devices;

an output data storing device which stores therein output image data outputted from the  
10 reconfiguration device; and

a reconfiguration table in which a relationship between addresses of a part of the output data areas outputted from the plural image processing/storing devices in accordance with each  
15 parameter and addresses of the output data storing device is stored in advance,

wherein said reconfiguration device reconfigures the image data, processed by the plural image processing/storing devices, by referring to the  
20 reconfiguration table.

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IMAGE DISTRIBUTING AND PROCESSING APPARATUS

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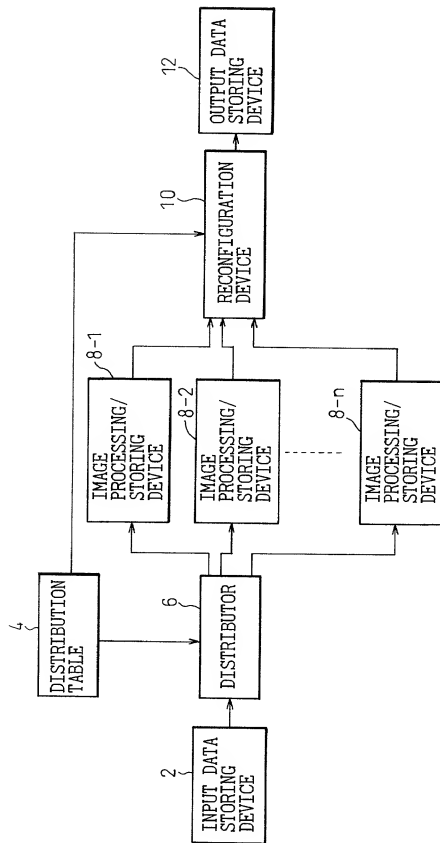
ABSTRACT OF THE DISCLOSURE

5  
10  
15  
20

An image distributing and processing apparatus is provided with a distributor which distributes image data outputted from an input data storing device into a plurality of image processing/storing devices in accordance with parameters to be used for the image processing operation, and a reconfiguration device which reconfigures the processed and stored output image data in a predetermined order, with reference to a distribution table. The reconfiguration device can refer to a reconfiguration table in which a relationship, between addresses of a part of output data areas outputted from the image processing/storing devices in accordance with each parameter and addresses of the output data storing device, is stored in advance, in place of the distribution table.

09304272-000700

Fig.1



$$\frac{2}{5}$$

Fig.2

4

DISTRIBUTION TABLE

2		8-1~8-n	
(x0, x1)		(a0, a1)	
(x2, x3)		(b0, b1)	
(x4, x5)		(a2, a3)	
(x6, x7)		(c0, c1)	
(x8, x9)		(b2, b3)	
(x10, x11)		(a4, a5)	

09291272 090799 662060 22212660



Fig.3

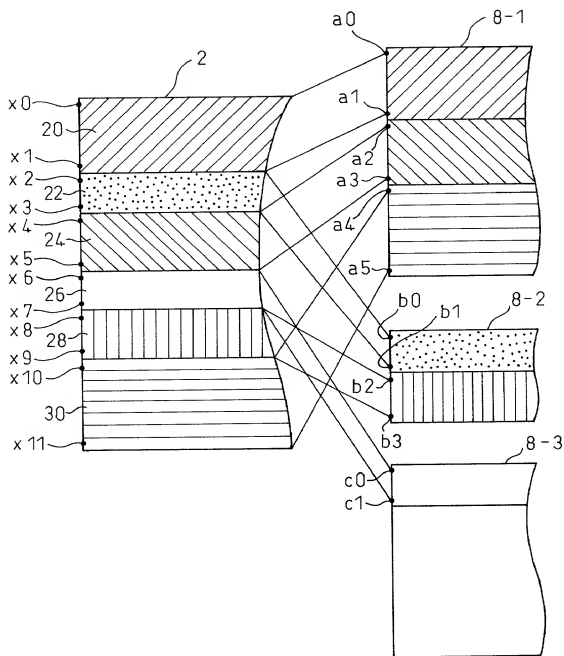


Fig.4

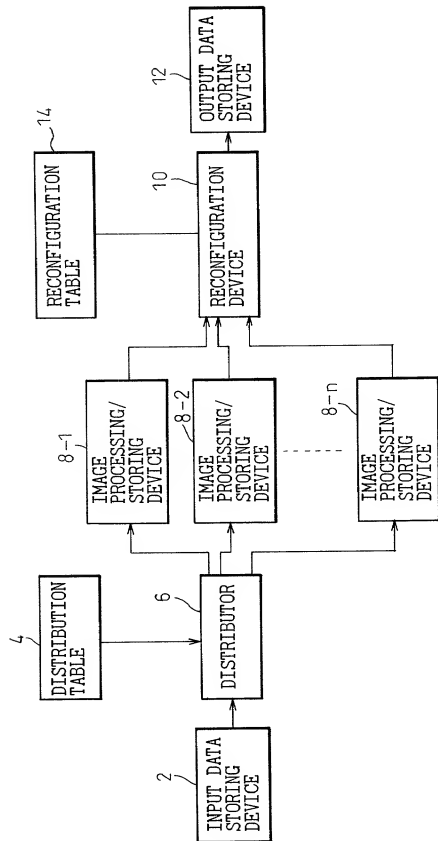


Fig.5

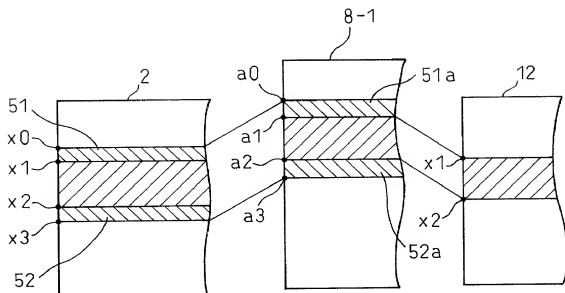


Fig.6

DISTRIBUTION TABLE

$x0 - x3$	$a0 - a3$

Fig.7

RECONFIGURATION TABLE

$a1 - a2$	$x1 - x2$

(Christie)

PTO/SB/106 (8-95)

Approved for use through 9/30/98. OMB 0651-0032

Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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## Declaration and Power of Attorney For Patent Application

## 特許出願宣言書及び委任状

## Japanese Language Declaration

## 日本語宣言書

下の氏名の発明者として、私は以下の通り宣言します。

As a below named inventor, I hereby declare that:

私の住所、私書箱、国籍は下記の私の氏名の後に記載された通りです。

My residence, post office address and citizenship are as stated next to my name.

下記の名称の発明に関して請求範囲に記載され、特許出願している発明内容について、私が最初かつ唯一の発明者（下記の氏名が一つの場合）もしくは最初かつ共同発明者であると（下記の名称が複数の場合）信じています。

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

IMAGE DISTRIBUTING AND PROCESSING

APPARATUS

上記発明の明細書（下記の欄でx印がついていない場合は、本書に添付）は、

the specification of which is attached hereto unless the following box is checked:

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as United States Application Number or  
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私は、特許請求範囲を含む上記訂正後の明細書を検討し、内容を理解していることをここに表明します。

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則典第37編第1条56項に定義されるとおり、特許審判の有用性について重要な情報を開示する義務があることを認めます。

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

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私は、米国法典第35編119条(a)-(d)項又は365条(b)項に基づき下記の、米国外の何の少なくとも一カ国を指定している特許協力条約365(a)項に基づく国際出願、又は外国での特許出願もしくは発明者証の出願についての外国優先権をここに主張するとともに、優先権を主張している。本出願の前に出願された特許または発明者証の外国出願を以下に、格内をマークすることで、示しています。

## Prior Foreign Application(s)

外国での先行出願

10-373347 (Pat. Appln.) Japan  
(Number) (Country)  
(番号) (国名)

(Number) (Country)  
(番号) (国名)

## Please see Attachment for additional prior foreign applications.

私：第35編米国法典119条(e)項に基づいて下記の米国外特許出願規定に記載された権利をここに主張いたします。

(Application No.) (Filing Date)  
(出願番号) (出願日)

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(Application No.) (Filing Date)  
(出願番号) (出願日)

(Application No.) (Filing Date)  
(出願番号) (出願日)

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I hereby claim foreign priority under Title 35, United States Code, Section 119 (a)-(d) or 365(b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed.

## Priority Not Claimed

優先権主張なし

28/December/1998  
(Day/Month/Year Filed)  
(出願年月日)

I hereby claim the benefit under Title 35, United States Code, Section 119(e) of any United States provisional application(s) listed below.

(Application No.) (Filing Date)  
(出願番号) (出願日)

I hereby claim the benefit under Title 35, United States Code, Section 120 of any United States application(s), or 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code Section 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of application.

(Status: Patented, Pending, Abandoned)  
(現況: 特許許可済、保属中、放棄済)

(Status: Patented, Pending, Abandoned)  
(現況: 特許許可済、保属中、放棄済)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

**Japanese Language Declaration**  
 (日本語宣言書)

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 手続を米特許商標局に対して遂行する弁護士または代理人  
 として、下記の者を指名いたします。(弁護士、または代理  
 人の氏名及び登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint  
 the following attorney(s) and/or agent(s) to prosecute this  
 application and transact all business in the Patent and Trademark  
 Office connected therewith (list name and registration number)  
 Please see attachment

書類送付先

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 Christie, Parker & Hale, LLP  
 350 West Colorado Boulevard, Suite 500  
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Full name of sole or first inventor  
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発明者の署名

日付

Inventor's signature

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第二共同発明者

Full name of second joint inventor, if any

第二共同発明者

日付

Second inventor's signature

Date

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国籍

Citizenship

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(第三以降の共同発明者についても同様に記載し、署名をす  
 ること)

(Supply similar information and signature for third and subsequent  
 joint inventors.)

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